

Docket No.: 57454-257

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Fukashi MORISHITA

Serial No.: Continuation of
Appln. Serial No. 09/739,227

Group Art Unit: Not yet assigned

Filed: November 15, 2001

Examiner: Not yet assigned

For: INTERNAL POWER SUPPLY VOLTAGE GENERATION CIRCUIT THAT CAN SUPPRESS
REDUCTION IN INTERNAL POWER SUPPLY VOLTAGE IN NEIGHBORHOOD OF LOWER
LIMIT REGION OF EXTERNAL POWER SUPPLY VOLTAGE

PATENT

11011 U.S. PTO
09/987566
11/15/01

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, DC 20231

Dear Sir:

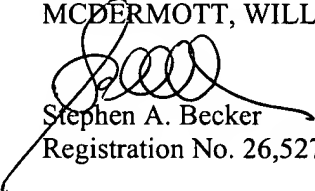
In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The references were cited by or submitted to the U.S. Patent and Trademark Office in parent application Serial No. 09/739,227, filed December 19, 2000, which is relied upon for an earlier filing date under 35 USC 120. Thus, copies of these references are not attached. 37 CFR 1.98(d).

Respectfully submitted,

MCDERMOTT, WILL & EMERY


Stephen A. Becker
Registration No. 26,527

600 13th Street, N.W.
Washington, DC 20005-3096
(202) 756-8000 SAB:blp
Date: November 15, 2001
Facsimile: (202) 756-8087